



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Reply, the Application of:

K. SOUMYANATH

Application No.: 09/896,345

Filed: June 28, 2001

For: **Area Efficient Waveform Evaluation and
DC Offset Cancellation Circuits**

Art Group: 2816

Examiner: Hai L. Nguyen

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: 9/29/05

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I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

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Complete if Known

Application Number	09/896,345
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Filing Date	June 28, 2001
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First Named Inventor	K. Soumyanath
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Art Unit	2816
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Examiner Name	Hai L. Nguyen
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Attorney Docket Number	42390P11206
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U.S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

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SignatureDate
Considered

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

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Based on PTO/SB/08A (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 08/11/2003.

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